

Docket No. 250798US2S

IN RE APPLICATION OF: Shinichiro SHIRATAKE et al.

SERIAL NO: 10/803,935

FILED: March 19, 2004

FOR: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND OPERATION METHOD THEREFOR



JPW

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

Transmitted herewith is an amendment under 37 C.F.R. § 1.111 in the above-identified application.

- ☒ No additional fee is required
- ☐ Small entity status of this application under 37 C.F.R. §1.9 and §1.27 is claimed.
- ☐ Additional documents filed herewith:

The Fee has been calculated as shown below:

CLAIMS	CLAIMS REMAINING		HIGHEST NUMBER PREVIOUSLY PAID	NO. EXTRA CLAIMS	RATE	CALCULATIONS
TOTAL	18	MINUS	20	0	x \$50 =	\$0.00
INDEPENDENT	2	MINUS	3	0	x \$200 =	\$0.00
		<input type="checkbox"/> MULTIPLE DEPENDENT CLAIMS			+ \$360 =	\$0.00
		TOTAL OF ABOVE CALCULATIONS				\$0.00
		<input type="checkbox"/> Reduction by 50% for filing by Small Entity				\$0.00
		<input type="checkbox"/> Recordation of Assignment			+ \$40 =	\$0.00
		TOTAL				\$0.00

- ☐ A check in the amount of \$0.00 is attached.
- ☐ Credit card payment form is attached to cover the fees in the amount of \$0.00
- ☒ Please charge any additional Fees for the papers being filed herewith and for which no check or credit card payment is enclosed herewith, or credit any overpayment to deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.
- ☒ If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time may be charged to Deposit Account No. 15-0030. A duplicate copy of this sheet is enclosed.

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.

Customer Number

22850

Tel. (703) 413-3000
Fax. (703) 413-2220
(OSMMN 05/03)

I:\ATTY\RFF\25S\250798\250798US-AM-CVR.DOC


Eckhard H. Kuesters

Registration No. 28,870

James D. Hamilton
Registration No. 28,421

DOCKET NO: 250798US2S



IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF :
SHINICHIRO SHIRATAKE ET AL. : EXAMINER: LE, T. Q.
SERIAL NO: 10/803,935 :
FILED: MARCH 19, 2004 : GROUP ART UNIT: 2827
FOR: SEMICONDUCTOR INTEGRATED :
CIRCUIT DEVICE AND
OPERATION METHOD THEREFOR

AMENDMENT UNDER 37 C.F.R. § 1.111

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

SIR:

In response to the Office Action dated April 18, 2005, please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 7 of this paper.